

IDS - 02/09/2004 ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Serial Interface to Flash-Memory Chip Using PCI-Express-Like Packets and Packed Data for Partial-Page Writes						
<p>Application Number :</p> <p>Confirmation Number:</p> <p>First Named Applicant: Horng-Yee Chou</p> <p>Attorney Docket Number: ML-27</p> <p>Art Unit:</p> <p>Examiner:</p> <p>Search string: (5835936 or 5822251 or 5430859 or 20030145141 or 20030142227 or 20030093606).pn</p>							
US Patent Documents							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
NTV	1	5835936	1998-11-10	Tomioka , et al.	B1	711	103
NTV	2	5822251	1998-10-13	Bruce , et al.	B1	365	185.13
NTV	3	5430859	1995-07-04	Norman , et al.	B1	711	103
US Published Applications							
Note: Applicant is not required to submit a paper copy of cited US Published Applications							
init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
NTV	1	20030145141	2003-07-31	Chen, Chien-An ; et al. July 31, 2003	A1	710	74
NTV	2	20030142227	2003-07-31	van Zee, Pieter J.	A1	348	333.1
NTV	3	20030093606	2003-05-15	Mambakkam, Sreenath ; et al.	A1	710	305
Signature							
Examiner Name				Date			
/Than Nguyen/ (08/04/2006)				08/04/2006			